

FIG. 1
PRIOR ART

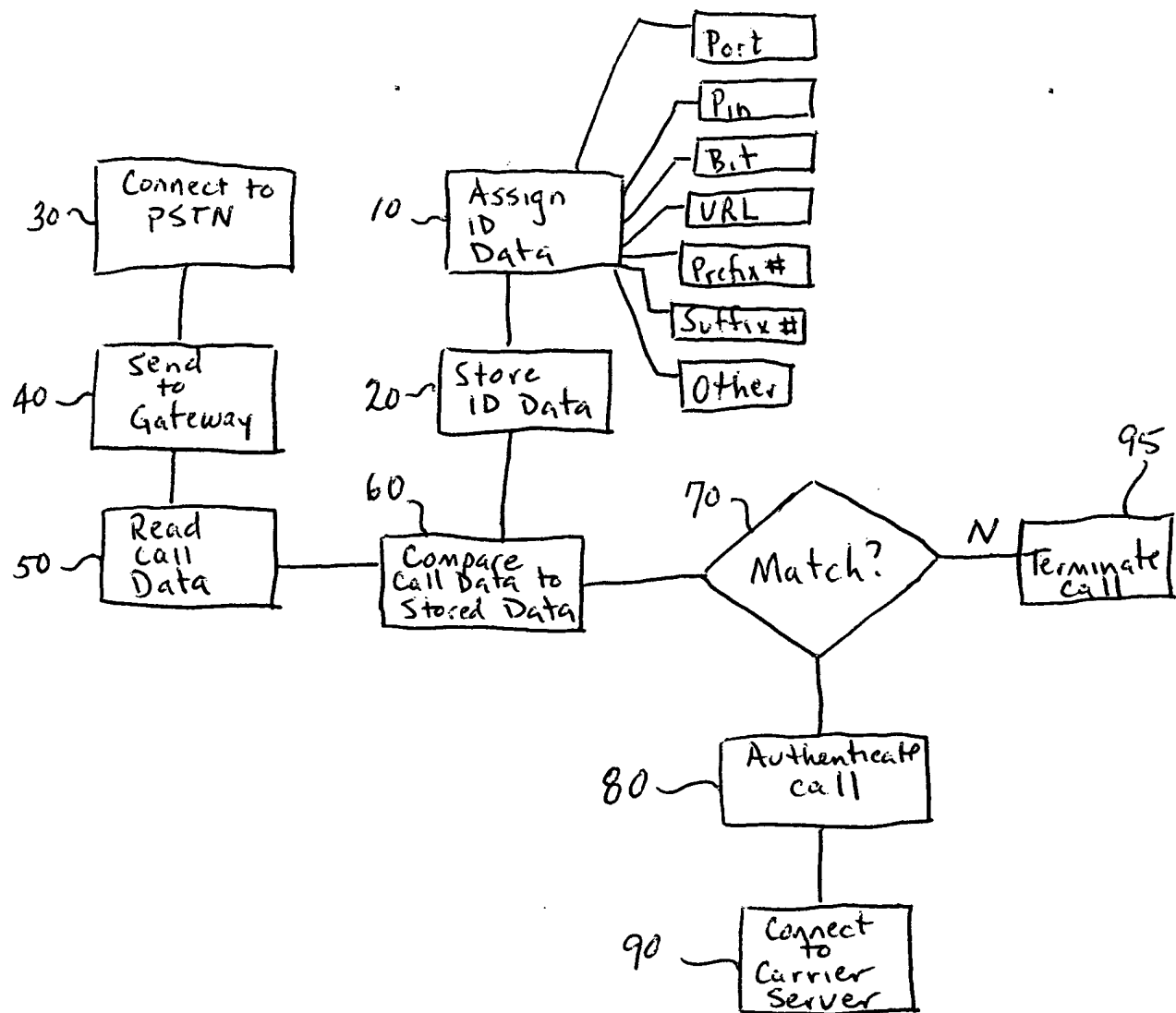


FIG. 2

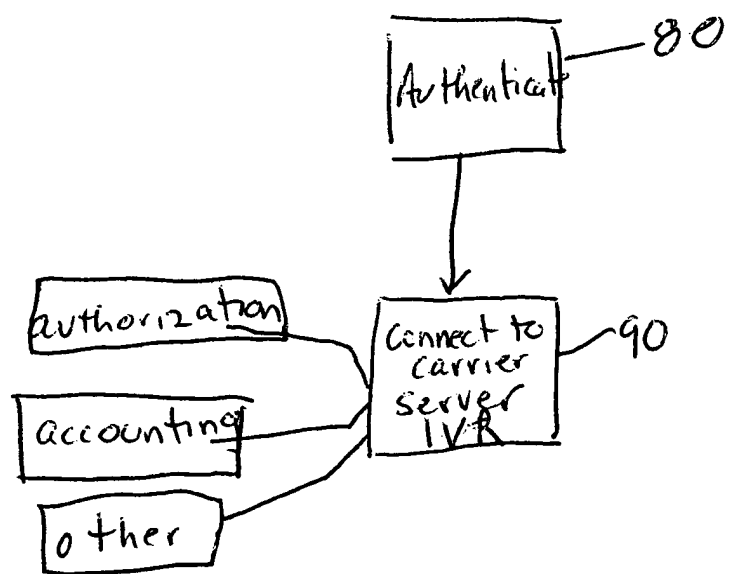


FIG. 3

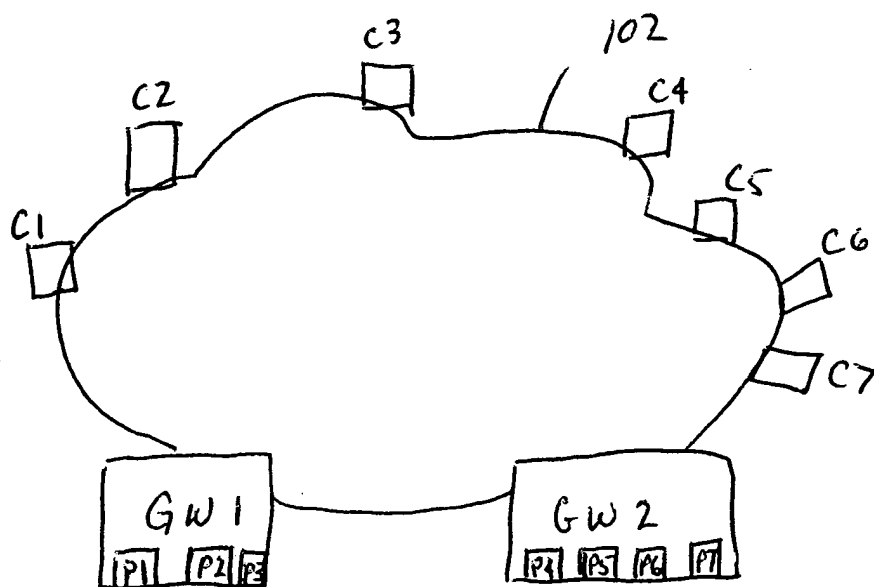


FIG. 4

port	P1	P2	P3	P4	P5	P6	P7
Carrier	C1	C2	C3	C4	C5	C6	C7

FIG. 5